

Sheet 1 of 1

Form PTO-1449

**INFORMATION DISCLOSURE CITATION
IN AN APPLICATION**

(Use several sheets if necessary)

Docket Number (Optional)

A5GN2392US

Application Number

101734,391

Applicant

Gentile, Ken

Filing Date

12/11/03

Group Art Unit

2816

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SE	6 1 9 8 3 5 3	3/6/01	Janesch, et al.	331	16	
SE	6 3 2 6 8 5 1	12/4/01	Staszewski, et al.	331	17	
SE	6 4 0 4 2 4 7	6/11/02	Wang	327	158	
SE	6 4 2 9 9 0 1	8/6/02	Kiyose, et al.	348	500	
SE	6 4 7 3 4 7 8	10/29/02	Wallberg, et al.	375	376	
SE	6 5 4 2 0 3 9	4/1/03	Ogura	331	11	
SE	6 5 9 3 8 1 5	6/15/03	Takahashi	331	1A	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Dunning, Jim, et al., "An All-Digital Phase-Locked Loop with 50-Cycle Lock Time Suitable for High-Performance Microprocessors", IEEE Journal of Solid-State Circuits Vol 30, No. 4, April, 1995, pp. 412-422

EXAMINER

Cassandra Coy

DATE CONSIDERED

June 23, 2005

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP§609; Draw line through